POPESCU ALEXANDRA

QA ENGINEER

SKILLS

ALM (HP Quality Center)

Jira

SQL

Gherkin

Functional testing

Integration and System Testing

Test Planning and Test Control

Test Implementation and Execution

Test Reporting

Bug management

LANGUAGES

English

Highly proficient in speaking and in writing

French

Good working knowledge

Spanish

Working knowledge

Romanian

Native

WORK EXPERIENCE

Jun 2019 – Present **QA Engineer** at **Canadian Bank Note**

Main responsibilities:

- Identify and analyze business requirements
- Write, update and execute test cases
- Test in different environments including desktop and web
- Carry out Integration Testing for the application
- Help maintaining the existing automation test suits with valid test data
- Document and manage bugs and issues using Jira
- Help the development team to correct bugs and errors
- Work effectively meeting deadlines in an Agile environment.

Jun 2015 - Jun 2019 **Test Specialist** at **IBM**

As a Test Specialist, my primary responsibility is to find bugs and issues within various ADM SOURCING SIT banking applications before it gets deployed to everyday users. Hence, I perform integration testing against client's functional and technical requirements and report defects to development teams. Main respnsabilities include delivering testing tasks in time, supporting development team by providing an objective feedback regarding codeflexibility and software design, performing integration tests against client's technical requirements to assure that they match the espected functionalities.

Nov 2014 - Jun 2015 **Electrical Distribution Designer** at **Renault Technologie Roumanie**

Main responsibilities:

- Create and modify electrical harness using Lcable platform for Logan and Duster vehicles.
- Updating electrical functional schemes in LCable, which form the automotive's electrical and electronic architecture.
- Adapt electrical equipments for vehicle engineering or for mechanical engineering(engine and gear boxes parts).

Jan 2014 - Nov 2014 **Technical Sales Estimator** at
Honeywell Internationals , Bucharest

Main responsibilities:
- Support front office in technical matters and supply bill of material along with
coresponding automation solution.
- Complete cost estimates and proposals within required timelines.
- Accurate portrayal of costs in the form of cost estimates.
- Advice to the Sales/Service Front Office Team on technical and commercial
alternatives that ensure lowest compliant bid while providing highest value to the
customer.
- Proposals and Estimates captured through the use of Siebel.
- Full proposal and costing of: All Honeywell hardware and software, One
Honeywell content, other Third party content, for All LSS efforts, all projects.

STUDIES AND CERTIFICATES

Oct 2015 – Apr 2021 **PhD Thesis in Electrical Engineering**

Politehnica University of Bucharest

RESEARCH ON THE ANALYSIS OF ANALOG CIRCUITS WITH MEMRISTORS

Oct 2013 - Jun 2015 **Master's degree**

Politehnica University of Bucharest

Applied informatics in electrical engineering

Oct 2009 - Jun 2013 **Bachelor's Degree**

Politehnica University of Bucharest

Applied Informatics in Electrical Engineering

COURSES

 Jul 2016 - Jul 2016   **ISTQB Foundation Level**

Functional testing course at IBM

PUBLICATIONS

1. Alexandra Ionescu, Alina Oroșanu, Andrei Dragomir, Andrei Roșu, Mihai Iordache, "Analysis of memristive nonlinear circuits", Electric Vehicles International Conference&Show, Octombrie 2017, ICPE Solar Park, București, Romania.

2. Alexandra Ionescu, Alina Oroșanu, Diana-Ramona Sănătescu, Mihai Iordache, "A new approach on Chua's circuit analysis" U.P.B. Sci. Bull., Series C, Vol. 81, Iss. 4, 2019 ISSN 2286-3540, 2019.

3. Alexandra Ionescu, Alina Oroșanu, Mihai Iordache, „Memristive model of the Hodgkin-Huxley axon", Proceeding of the 11th International Symposium Advanced Topics in Electrical Engineering – ATEE'21, March 28-30, 2019, Bucharest, Romania.

4. Alina Oroșanu, Andrei Dragomir, Andrei Roșu, Alexandra Ionescu, Mihai Iordache, " Dynamic Windshield Sun Shade Assistence", Electric Vehicles International Conference&Show, Octombrie 2017, ICPE Solar Park, București, Romania.

5. Diana-Ramona Sănătescu, Lucian-Vasile Ene, Alexandra Ionescu, Alina Oroșanu, Mihai Iordache, "A new approach for nonlinear analog circuits analysis" U.P.B. Sci. Bull., Series C, Vol. 81, Iss. 3, 2019 ISSN 2286-3540, 2019.

6. Mihai Iordache, Marilena Stănciulescu, Diana Sănătescu, Sorin Deleanu, Alexandra Ionescu, Anastasie Moscu , Lavinia Bobaru, „CSAP and TFSG – Circuit Symbolic Analysis Programs", SIELEM 2019 - 12-th International Conference on Electromechanical and Power Systems10 – 11 Octombrie 2019, Chișinău Rep. MOLDOVA