Alexandru SERBANESCU

Born on 02.03.1943 Address: 25, Biserica Amzei, Str., Et. 6, Ap.26, Sector 1, Bucharest, ROMANIA

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CURRICULUM VITAE

Formation

MS in Telecommunications (1965), **Ph.D.** in IT&C (1974), from "**Politehnica**" University of Bucharest, Romania

Experience

Engineer in a Telecom Factory (1965-1967), Bucharest, Romania Scientific researcher in the "Institute of Research for Telecommunications" (1967-1982), Bucharest, Romania

Assistant Professor at "Politehnica" University, Bucharest ('66/'91). Assistant Professor at Military Technical Academy Bucharest, (1981/'91) Professor at Military Technical Academy (1991/-) Bucharest, Romania Doctoral Advisor in IT&C (1991/-), Military Technical Academy, Bucharest, Professor emeritus at Military Technical Academy (2010/-), Bucharest, Romania

Main research interests:

- Digital Signal Processing
- Computer Aided Design and Optimization based on Artificial Intelligence
- Systemic Approach in IT&C
- Dynamic Systems: Chaos based Communications, Control of chaos.
- Chaos based Encryption;

- Nonlinear System Analysis using Recurrence Plot Analysis (**RPA**) and **RQA Publications:**

- Author or co-author of more than **200 articles** and **scientific papers**
- Author or co-author of **20 edited books** e.g.

"Switched Capacitor Circuits", 1987, Ed. Militara, Bucharest, Romania "Le traitement du signal sous Matlab", 2000, Hermes, France

"Development of some encryption algorithms using chaos", 2011, LAMBERT Acad. Publ.

"Dynamic Systems - Applications in Communications", 2004, Bucharest,

"Systemes et Signaux face au Chaos" App. aux Communucation., Bucharest, Romania "Analiza neliniara a seriilor de timp", 2014, Ed. ATM, Bucharest, Romania

Programming languages: C++, **PSpice (OrCAD)**, **MATLAB** and **LabView Languages:** English, French

IEEE Senior Member from 1994 (Signal Processing, Communications and Computational Intelligence IEEE Societies)

Details on http://alexserbanescu.ro/