


# Paul Diaconescu



## EXPERIENTA PROFESIONALA:

### Senior Development Manager

**Infineon** (Bucuresti): 2020-06 - prezent

### Director Marketing

**eSilicon Corporation** (Bucuresti): 2019-07 – 2020-06

### Senior Marketing Manager

**eSilicon Corporation** (Bucuresti): 2011-08 - 2019-06

### Senior Consultant

**Oracle** (Bucuresti): 2008-04 - 2011-08

### ASIC Verification Engineer

**Infineon** (Bucuresti): 2005-10 - 2008-04

### ASIC Verification Engineer

**NOBUG Consulting** (Bucuresti): 2004-09 - 2005-09

### Network Administrator

**RTC Holding** (Bucuresti): 2003-07 - 2004-09


## EDUCATIE

**Stagiu Doctorat – „Contributii la Optimizarea și Aplicatiile Modelului Deep Convolutional Neural Networks”, Facultatea de Electronica și Telecomunicatii, Universitatea Politehnica Bucuresti, Bucuresti (2015 - prezent)**

**Master – Master of Business Administration  
Tiffin University, Bucuresti (2009-2011)**

**Studii Aprofundate – Ingineria Sistemelor de Calcul  
Facultatea de Electronica și Telecomunicatii, Universitatea Politehnica Bucuresti Bucuresti  
(2004 - 2005)**

**Universitate – Electronica aplicata, Ingineria Informatiei  
Facultatea de Electronica și Telecomunicatii, Universitatea Politehnica Bucuresti Bucuresti  
(1999 - 2004)**



**Colegiul National Ion Minulescu, Slatina (1995 - 1999)**

**CURSURI**

**Infineon Leadership Excelence Program (ILEP 2)**– Infineon (2021)  
**Automotive Chip Development** – Infineon (2020)  
**eSilicon Leadership Program** – eSilicon (2016-2018)  
**IP Sales – Legal Terms and Conditions** – eSilicon (2015)  
**Agile Development** – eSilicon (2013)  
**Project Management** – eSilicon (2012)  
**Oracle Application Integration Architecture (AIA)** – Oracle (2010)  
**Influencing and Persuasion** – Oracle (2010)  
**Advanced Design and Verification Training** – Cadence, Germania (2006)  
**Functional Verification Methodology** – Doulos, Germania (2006)  
**Failure Mode and Effects Analysis** – Infineon, Germania (2005)  
**Security and Chip Card ICs Training** – Infineon, Germania (2005)  
**Cisco Networking Academy** – Bucuresti (2002 – 2003)

**ARTICOLE PUBLICATE SAU ACCEPTATE PENTRU PUBLICARE**

**P. Diaconescu, V. E. Neagoe**, “A Higly Configurable Deep Learning Architecture for Hyperspectral Image Classification,” *Proc. IEEE 13th International Symposium on Applied Computational Intelligence And Informatics (SACI 2019)*, Timisoara, ROMANIA, May 29-31, 2019, pp.197-200, WOS:000610436600035

V. E. Neagoe, **P. Diaconescu**, “An Ensemble of Deep Convolutional Neural Networks for Drunkenness Detection Using Thermal Infrared Facial Imagery,” *Proc. 13th International Conference on Communications (COMM2020)*, Bucharest, Romania, Electr Network, June 19-20, 2020, pp. 147-150, WOS:000612723900026

V. E. Neagoe, **P. Diaconescu**, “CNN Hyperspectral Image Classification Using Training Sample Augmentation with Generative Adversarial Networks,” *Proc. 13th International Conference on Communications (COMM2020)*, Bucharest, Romania, Electr Network, June 19-20, 2020, pp.515-519, WOS:000612723900091

**P. Diaconescu, V. E. Neagoe**, “Credit Scoring Using Deep Learning Driven by Optimization Algorithms,” *Proc. 12th International Conference on Electronics, Computers and Artificial Intelligence (ECAI-2020)*, Bucharest, Romania, June 25-27, 2020, pp.1-6, WOS:000627393500021

**P. Diaconescu, V. Neagoe**, „A Deep Learning Approach to Autonomous Driving in Urban Environment”, accepted by Scientific Bulletin of the Politehnica University of Bucharest, Series C, Electrical Engineering and Computer Science, to be indexed in Web of Science