



Curriculum Vitae Europass



Personal information

First name(s) / Surname(s)

SVASTA Paul

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paul.svasta@cetti.ro; paul.svasta@apte.org.ro

Nationality

Romanian

Gender

Male

Work experience

Dates

1968 →

Occupation or position held

Professor

Main activities and responsibilities

Education

Name and address of employer

Electronic Technology and Reliability Department - POLITEHNICA University of Bucharest (UPB), Bucharest, Romania

Dates

1995 →

Occupation or position held

Head of Center

Name and address of employer

Center for Technological Electronics and Interconnection Techniques (UPB-CETTI), Bucharest, Romania

Type of business or sector

Education & Research

Dates

2011 →

Occupation or position held

President and Founder

Name and address of employer

ELINCLUS - ELectronic INnovation CLUSter, Bucharest, Romania

Type of business or sector

Research

Dates

2011 →

Occupation or position held

Member of the board

Name and address of employer

CLUSTERO, Romanian Clusters Association, Bucharest, Romania

Type of business or sector

Research

Dates

1999 -2010

Occupation or position held

Member of the board

Main activities and responsibilities

R&D management, innovation & TT support

Name and address of employer

Agency for Management of Scientific Research and Innovation Transfer (AMCSIT) Bucharest

Type of business or sector

Research, development, innovation and technological transfer

Dates

1994 – 2007, 2009 →

Occupation or position held	Vicepresident
Name and address of employer	Romanian Association for Electronic and Software Industry - ARIES
Type of business or sector	Electronic industry
Dates	2003 →
Occupation or position held	President and Founder
Name and address of employer	Association for Promotion of Electronics Technology - APTE
Type of business or sector	electronics
Dates	2001 - 2005
Occupation or position held	Expert (attested for evaluation of projects proposals)
Name and address of employer	Ministry for Education and Research
Dates	2001 - 2003
Occupation or position held	President of Steering Committee for the Multi User Research Center projects, program supported by the World Bank
Name and address of employer	Ministry for Education and Research
Dates	1999 - 2000
Occupation or position held	Advisor of the President
Name and address of employer	National Agency for Science, Technology and Innovation, today Romanian Ministry for Research, Bucharest, Romania
Dates	11/1998 - 12/1998
Main activities and responsibilities	Scholarship DAAD (German Academic Exchange Service)
Name and address of employer	Technische Universität Darmstat- Germany
Dates	10/1997 - 12/1997
Main activities and responsibilities	Research activity
Name and address of employer	Fraunhofer Institut für Festkoerperertechnologie, München Germany
Dates	1982 - 1990
Occupation or position held	Project Manager for student training
Main activities and responsibilities	organize courses and laboratory at Electronics Technology, practice
Name and address of employer	Electronic Passive Components Manufacturing Company, Curtea de Arges, Romania
Dates	1978 - 1982
Occupation or position held	Project Manager for Development of Score Boards
Main activities and responsibilities	Development and research activities in designing and providing equipment for collecting, processing and displaying data from sporting events
Name and address of employer	Ministry of Youth and Sport
Dates	1973 - 1974
Occupation or position held	DAAD (German Academic Exchange Service) Scholarship
Name and address of employer	Technische Universitaet München, Institut für Nachrichtetechnik, Munich, Germany

Dates 1971 - 1973
 Occupation or position held Manager for student hands-on activities
 Main activities and responsibilities student hands-on activities
 Name and address of employer "ELECTRONICA" Radio & TV Manufacturing Enterprise, Bucharest, Romania

Education and training

Dates 1981
 Title of qualification awarded Ph.D. Degree
 Principal subjects / occupational skills covered Electronics circuits; reliability
 Name and type of organisation providing education and training Faculty of Electronics and Telecommunications, POLITEHNICA University of Bucharest, Romania

Dates 1963 - 1968
 Title of qualification awarded Bachelor and Master degrees
 Principal subjects / occupational skills covered Electronics and Telecommunications
 Name and type of organisation providing education and training Faculty of Electronics and Telecommunications, POLITEHNICA University of Bucharest, Romania

Dates 1960 - 1962
 Name and type of organisation providing education and training Military Academy in Telecommunications, Sibiu , Romania

Dates 1956 - 1960
 Name and type of organisation providing education and training Military Navy High School, Galati, Romania

Personal skills and competences

Mother tongue(s) **Romanian**

Other language(s)

Self-assessment
 European level (*)

English
German
Hungarian

Understanding				Speaking				Writing	
Listening		Reading		Spoken interaction		Spoken production			
C1	Proficient user	C1	Proficient user	B1	Independent user	B1	Independent user	B1	Independent user
C1	Proficient user	B2	Independent user	C2	Proficient user	C2	Proficient user	B1	Independent user
B2	Independent user	A1	Basic User	B1	Independent user	A2	Utilizator elementar	A1	Basic User

(*) [Cadrlui european comun de referință pentru limbi](#)

Social skills and competences Team spirit. Good ability to adapt to multi-cultural environments gained through my work experience abroad.
 Good communications skills gained through my experience as conferences session chairman or international conference co-chairman.
 Networking activities in order to gather Romanian universities involved in research and education

	focused on electronic microsystem packaging.
Organisational skills and competences	<p>Sense of organization, good experience in project team management leadership currently responsible for a team of 20 peoples.</p> <p>Initiator (1992) and manager of the national student contest TIE (Interconnection Techniques in Electronics) – www.tie.ro; co-ordination of a large numbers of training development projects.</p> <p>Initiator (1994) and General Chair of the International Symposium for Design and Technology in Electronic Packaging (SIITME), IEEE conference since 2009.</p> <p>Development and implementation of student curricula based on hands-on skills within the framework of Electronic Passive Components Company (Curtea de Arges). More than 3000 students have used the training program.</p> <p>Introduction of multimedia and e-learning in classical (traditional) teaching.</p> <p>Development and implementation of regional technical events (symposium, workshops, conferences) focused on electronic technology topics including assembling technologies.</p> <p>Project implementation monitoring for World Bank supported research program at Romanian university level.</p> <p>Development, implementation and monitoring of the national grant program for research. Initiator and President of ELINCLUS, ELectronic INnovation CLUSter – Bronze Label by ESCA</p>
Technical skills and competences	<p>Good command in quality control processes. Responsible for implementation of Management system ISO 9001.</p> <p>Good experience in Thick Film Technology gained during DAAD scholarship in Fraunhofer IFT Munich, Germany.</p> <p>Development of Ph.D. program in electronic packaging topics.</p>
Computer skills and competences	<p>Knowledge in Microsoft Office tools (Word, Excel and Power Point).</p> <p>Development of computer aided learning systems; multiple choice web based knowledge testing technology.</p>
Artistic skills and competences	<p>Senior editor for some technical newspapers (Electronica Azi – Electronics Today; EP&Dee – Electronic Products & Design Eastern Europe)</p> <p>Editor of the Electronic Packaging serie of books</p>
Other skills and competences	Involved in some NGO or professional organizations (IEEE, IMAPS, IPC, FLUIDAS)
Driving licence	Category B

Appendix

Appendix 1 - Scientific Papers, published treaties, monographs, books, brevets

Appendix 2 – Expertise in national/international programs

Appendix 3 - Member of the professional associations

**Scientific Papers
(2012-2019)
Prof. PAUL SVASTA, Ph.D.**

1. **P. Svasta**, N.D. Codreanu, Requirements of Industry for the Education of Printed Circuit Board Designers, 20 FED Konferenz, "Elektronik-Design-Leiterplatten-Baugruppen 2012", Dresden 20-22 sept. 2012, pp 155-164
2. D. Bonfert, N. Codreanu, P. Svasta, C. Ionescu, Optimization of a Heater Geometry for Flexible Gas Sensor Applications, 36th International Spring Seminar on Electronics Technology - ISSE 2013: Automotive Electronics, May 8 – 12, 2013, Alba Iulia, Romania
3. D. Bonfert, D. Hemmetzberger, G. Klink, K. Bock, C. Ionescu, P. Svasta, Electrical Stress on Transparent Conductive Oxide Layer, 36th International Spring Seminar on Electronics Technology - ISSE 2013: Automotive Electronics, May 8 – 12, 2013, Alba Iulia, Romania
4. M. Pantazica, C. Marghescu, C. Tamas, H. Wohlrabe, K. Wolter, P. Svasta, Evaluation of the Effects of the Layout Design on the Quality of SMT Boards, 36th International Spring Seminar on Electronics Technology - ISSE 2013: Automotive Electronics, May 8 – 12, 2013, Alba Iulia, Romania
5. M. Brânzei, I. Pencea, I. Plotog, M. Branzei, A. Bibis, P. Svasta, Multiconvolutional Approach for Uncertainty Estimation of the Thermal Diffusivity Measured by Flash Method, 36th International Spring Seminar on Electronics Technology - ISSE 2013: Automotive Electronics, May 8 – 12, 2013, Alba Iulia, Romania
6. B. Mihailescu, I. Plotog, **P. Svasta**, C. Marghescu, „Modeling and Simulation of Inductive Structures for Non-Invasive Medical Uses”, Conference proceedings 2013 IEEE 19th International Symposium for Design and Technology of Electronics Packages (SIITME), pp. 167-170, IEEE eXplore DOI: 10.1109/SIITME.2013.6743666, ISBN: 978-1-4577-1275-3, Galați, România, Octombrie 2013
7. F. Draghici, M. Pantazica, N. Codreanu, **P. Svasta**; Hands-on Project – Key Issue in the Education of Future Electronic Engineers, 37th International Spring Seminar on Electronics Technology, ISSE 2014, May 7-11, 2014, Dresda, Germany
8. A. Marghescu, **P. Svasta**, T. Neacsă; Cryptographic Coprocessor for Data Integrity Algorithms, 37th International Spring Seminar on Electronics Technology, ISSE 2014, May 7-11, 2014, Dresda, Germany.
9. Bogdan Mihailescu, **Paul Svasta**, Alexandru Borcea, „*ELINCLUS Cluster - Bridge between Academia and Industry as an Innovation Accelerator*”, Conference proceedings 38th International Conference of IMAPS-CPMT Poland (IMAPS-CPMT 2014), Rzeszów - Czarna, 21-24 September 2014
10. Paul Svasta, Layout Design of Printed Circuits Boards – An Important Issue of the 21st Century Undergraduate Electronics Curricula, 38th International IMAPS-CPMT Poland Conference & Exhibition, Rzeszow-Czarna, 21-24 september 2014, Rev. Elektronika nr. 3/2015, ISSN 0033-2089, pg. 46-49
11. E. SENYUREK, Zs. ILLYEFALVI-VITÉZ, **P. SVASTA**, and A. PIETRIKOVA, E. ORUN, Virtual and Practical Applications to Electronic Assembling Technology, 2014 IEEE 20th International Symposium for Design and Technology in Electronic Packaging, IEEE eXplore DOI: 10.1109/SIITME.2014.6967033, ISBN: 978-1-4577-1275-3, Bucharest, România, Octombrie 2014
12. N. Bădălan, **P. Svasta**, Thermo-Electric Cooler for Power LEDs, 2014 IEEE 20th International Symposium for Design and Technology in Electronic Packaging, IEEE eXplore DOI: 10.1109/SIITME.2014.6967033, ISBN: 978-1-4577-1275-3, Bucharest, România, Octombrie 2014
13. A. Marghescu, G. Teseleanu, D.-S. Maimut, T. Neacsă and **P. Svasta**, Adapting a Ring Oscillator-based True Random Number Generator for Zynq System on Chip Embedded Platform, 2014 IEEE 20th International Symposium for Design and Technology in Electronic Packaging, IEEE eXplore DOI: 10.1109/SIITME.2014.6967033, ISBN: 978-1-4577-1275-3, Bucharest, România, Octombrie 2014
14. A. Marghescu, G. Teseleanu, and **P. Svasta**, Cryptographic Key Generator Candidates based on Smartphone built-in Sensors, 2014 IEEE 20th International Symposium for Design and Technology in Electronic Packaging, IEEE eXplore DOI: 10.1109/SIITME.2014.6967033, ISBN: 978-1-4577-1275-3, Bucharest, România, Octombrie 2014
15. C. Ropoteanu, **P. Svasta**, C. Ionescu, Planar Windings Design Analysis regarding Power Transfer Aspects of a Ferrite Core Planar Transformer in the MHz Region, 2014 IEEE 20th International Symposium for Design and Technology in Electronic Packaging, IEEE eXplore DOI: 10.1109/SIITME.2014.6967033, ISBN: 978-1-4577-1275-3, Bucharest, România, Octombrie 2014
16. A. Rizoiu, S. Enache, M. Varlam, and **P. Svasta**, A Fundamental Study of Supercapacitive Cells, 2014 IEEE 20th International Symposium for Design and Technology in Electronic Packaging, IEEE eXplore DOI: 10.1109/SIITME.2014.6967033, ISBN: 978-1-4577-1275-3, Bucharest, România, Octombrie 2014
17. N. Bădălan, **P. Svasta**, Peltier Elements vs Heat Sink in Cooling of High Power Leds, 38th International Spring Seminar on Electronics Technology "Novel Trends in Electronics Manufacturing", Eger, Hungary, May 6-10, 2015
18. M. Branzei, G. Varzaru, **P.M. Svasta**, Shear Strength Function of Temperature for Lead/Lead-Free Alloys Samples Obtained with Different Cooling Rate, 38th International Spring Seminar on Electronics Technology "Novel Trends in Electronics Manufacturing", Eger, Hungary, May 6-10, 2015

19. A. Marghescu, **P. Svasta**, E. Simion, Randomness Extraction Techniques for Jittery Oscillators, 38th International Spring Seminar on Electronics Technology "Novel Trends in Electronics Manufacturing", Eger, Hungary, May 6-10, 2015
20. A. Botau, D. Bonfert, C. Negrea, **P. Svasta**, C. Ionescu, Electro-Thermal Analysis of Flexible Micro-Heater, 38th International Spring Seminar on Electronics Technology "Novel Trends in Electronics Manufacturing", Eger, Hungary, May 6-10, 2015
21. B. Mihailescu, **P. Svasta**, C. Marghescu, A. Borcea, Clusters as a Catalyst for Developing Human Resources for the Internet of Things Industry, 38th International Spring Seminar on Electronics Technology "Novel Trends in Electronics Manufacturing", Eger, Hungary, May 6-10, 2015
22. M. G. Vidrascu, **P. M. Svasta**, M. Vladescu, Maintenance-free, super-capacitor based WSN power supply, Advanced Topics in Optoelectronics, Microelectronics, and Nanotechnologies VIII, Proc. of SPIE Vol. 10010, 100101F · © 2016 SPIE · CCC code: 0277-786X/16/\$18 · doi: 10.1117/12.2246114
23. Niculina Badalan, **Paul Svasta**, F. Drăghici, CONSTANT CURRENT VERSUS PULSE CURRENT FOR POWER SUPPLIES ON HIGH POWER LED, **2015 IEEE 21st International Symposium for Design and Technology of Electronics Packaging (SIITME)**, Brasov, Romania, 22-25.10.2015, PP. 263 – 266
24. 3. R. Negroiu, **P. Svasta**, Al. Vasile, C. Ionescu, C. Marghescu, Comparison between Zubieta Model of Supercapacitors and their Real Behavior, 22nd International Symposium for Design and Technology in Electronic Packaging 20-23 October, 2016 Baile Felix, Oradea, Romania
25. M. G. Vidrascu, **P. M. Svasta**, M. Vladescu, High Reliability Wireless Sensor Node for Bee Hive Monitoring, 22nd International Symposium for Design and Technology in Electronic Packaging 20-23 October, 2016 Baile Felix, Oradea, Romania
26. Supercapacitors - an Alternative Electrical Energy Storage device, By: **Svasta, P.**; Negroiu, R.; Vasile, Al., Conference: 5th International Symposium on Electrical and Electronics Engineering (ISEEE) Location: Galati, ROMANIA Date: OCT 20-22, 2017, 2017 5TH INTERNATIONAL SYMPOSIUM ON ELECTRICAL AND ELECTRONICS ENGINEERING (ISEEE) Book Series: International Symposium on Electrical and Electronics Engineering Published: 2017
27. Maintenance-free IOT Gateway Design for Bee Hive Monitoring, By: Vidrascu, M. G.; **Svasta, P. M.**, 2017 IEEE 23RD INTERNATIONAL SYMPOSIUM FOR DESIGN AND TECHNOLOGY IN ELECTRONIC PACKAGING (SIITME) Pages: 183-187 Published: 2017, 978-1-5386-1626-0/17/\$31.00 ©2017 IEEE
28. Embedded Software for IOT Bee Hive Monitoring Node, M. G. Vidrascu, **P. M. Svasta**, 2017 IEEE 23RD INTERNATIONAL SYMPOSIUM FOR DESIGN AND TECHNOLOGY IN ELECTRONIC PACKAGING (SIITME), Pages: 183-188, 978-1-5386-1626-0/17/\$31.00 ©2017 IEEE
29. Vasile, A. ; Codreanu, N. ; Dima, M. ; Ionescu, C. ; Negroiu, R. ; **Svasta, P.** s.a. "Fast Control System and Algorithms for Stabilizing of Mobile Platforms", 2018 41ST INTERNATIONAL SPRING SEMINAR ON ELECTRONICS TECHNOLOGY (ISSE), IEEE : International Spring Seminar on Electronics Technology ISSE Published: 2018, Conference: 41st International Spring Seminar on Electronics Technology (ISSE) Location: Zlatibor, SERBIA, Date: MAY 16-20, 2018 Accession Number: ISBN:978-1-5386-5731-7; 978-3-319-73847-5, ISSN: 2161-2536
30. Methods for Determining the Leakage Current of Supercapacitors, By: Negroiu, R.; **Svasta, P.**; Vasile, Al.; et al., Conference: 41st International Spring Seminar on Electronics Technology (ISSE) Location: Zlatibor, SERBIA Date: MAY 16-20, 2018, 2018 41ST INTERNATIONAL SPRING SEMINAR ON ELECTRONICS TECHNOLOGY (ISSE) Book Series: International Spring Seminar on Electronics Technology ISSE Published: 2018
31. D.C. Vasile, **P. Svasta**, Innovative Conductive Mesh Structure for the Protection of Security Electronic Circuits, Electronics System-Integration Technology Conference, Dresden, 2018.
32. Rodica Negroiu; **Paul Svasta**; Alexandru Vasile; Ciprian Ionescu; Popescu Ileana Iulia, The Performance of Supercapacitors' Main Parameters According to Topology of the Electrical Circuits in Which They are Used Publication Year: 2018, Page(s):187- 190, Date of Conference: 25-28 Oct. 2018, Date Added to IEEE Xplore: 03 January 2019, ISBN Information: Publisher: IEEE, Conference Location: Iasi, Romania
33. Monica Ciolacu, Ali Fallah Terhani, Leon Binder, **Paul Svasta**: "Education 4.0 - Artificial Intelligence assisted higher Education: Early recognition System with Machine Learning to support Students Success", SIITME 2018, IEEE Conference and Symposium, accepted for oral session, 2018. <https://ieeexplore.ieee.org/document/8599203>, Electronic ISBN: 978-1-5386-5577-1, Page 23-30
34. D.C. Vasile, **P. Svasta**, Antitamper Conductive Mesh Used for Securing Cryptographic Modules, 2018 IEEE 24th International Symposium for Design and Technology in Electronic Packaging (SIITME), Iasi, 2018.
35. Ciprian Ionescu, Alexandru Vasile, **Paul Svasta**, Norocel Codreanu, Mihai Jurbă, Gheorghe Marin, Comparative Performance Analysis of Dye Sensitized Solar Cells, 2019 IEEE 25th International Symposium for Design and Technology of Electronics Packaging (SIITME), Cluj Napoca, Romania, 23-26.10.2019, Page 3
36. Madalin Vasile Moise, Paul Svasta and Laurentiu Mihai Ionescu, Implementation of a prototype air quality measurement system using MEMS sensors, 2019 IEEE 25th International Symposium for Design and Technology of Electronics Packaging (SIITME), Cluj Napoca, Romania, 23-26.10.2019, Page 23

37. D. C. Vasile, **P. M. Svasta**, M. Pantazică, Preventing the Temperature Side Channel Attacks on Security Circuits, 2019 IEEE 25th International Symposium for Design and Technology of Electronics Packaging (SIITME), Cluj Napoca, Romania, 23-26.10.2019, Page 85
38. D. C. Vasile, **P. M. Svasta**, Protecting the Secrets: Advanced Technique for Active Tamper Detection Systems, 2019 IEEE 25th International Symposium for Design and Technology of Electronics Packaging (SIITME), Cluj Napoca, Romania, 23-26.10.2019, Page 87
39. C. Cherciu, A.N. Croitoru, **P.M. Svasta** and D.I. Nastac, Analysis of Real Time Corrections for a Global Navigation Satellite System Ground Station, 2019 IEEE 25th International Symposium for Design and Technology of Electronics Packaging (SIITME), Cluj Napoca, Romania, 23-26.10.2019, Page 105
40. Florin Berinde, Aurelian Kotlar, and **Paul Svasta**, Parametric Filter Design for Conducted Emissions, 2019 IEEE 25th International Symposium for Design and Technology of Electronics Packaging (SIITME), Cluj Napoca, Romania, 23-26.10.2019, Page 123
41. C. Ropoteanu and **P. Svasta**, Effects of Excitation Waveform on Developing Planar Core Transformers, 2019 IEEE 25th International Symposium for Design and Technology of Electronics Packaging (SIITME), Cluj Napoca, Romania, 23-26.10.2019, Page 135
42. N. Bădălan (Drăghici), **P. Svasta**, C. Marghescu, Thermal Model for LED Luminaire, 2019 IEEE 25th International Symposium for Design and Technology of Electronics Packaging (SIITME), Cluj Napoca, Romania, 23-26.10.2019, Page 147
43. Monica Ciolacu, Leon Binder, **Paul Svasta**, Ioan Tache, Dan Stoichescu, Education 4.0 - Jump to Innovation IoT in Higher Education, 2019 IEEE 25th International Symposium for Design and Technology of Electronics Packaging (SIITME), Cluj Napoca, Romania, 23-26.10.2019, Page 191
44. Vidrascu, Mihai; **Svasta, Paul**, IoT BEE HIVE MONITORING SYSTEM, UNIVERSITY POLITEHNICA OF BUCHAREST SCIENTIFIC BULLETIN SERIES C-ELECTRICAL ENGINEERING AND COMPUTER SCIENCE, Volume 82, Issue 2, Page 189-200, Published 2020
45. C. Ropoteanu, **P. Svasta**, Influence of Core Temperature on the Efficiency of a Planar Transformer, 43rd International Spring Seminar on Electronics Technology - Trends in Microelectronics Packaging and Interconnection Technology, May 14 – 15, 2020, Demanovska Valley – Slovakia (on-line)
46. M. I. Andreea, B. Mihailescu, **P. Svasta**, J. Hehlgans and D. Pupeza, Self-Switching Antenna for Higher Efficiency in RFID applications, 43rd International Spring Seminar on Electronics Technology - Trends in Microelectronics Packaging and Interconnection Technology, May 14 – 15, 2020, Demanovska Valley – Slovakia (on-line)
47. M. V. Moise, P. M. Svasta and A. G. Mazare, Programable IoT Pills Dispenser, 43rd International Spring Seminar on Electronics Technology - Trends in Microelectronics Packaging and Interconnection Technology, May 14 – 15, 2020, Demanovska Valley – Slovakia (on-line)
48. **D. C. Vasile, S. Chițu, P. Svasta**, *Cryptographic Key Derivation from an Anti-Tamper Solution*, 2020 8th Electronic System-Integration Technology Conference (ESTC), Vestfold, Norway, 2020
49. Sorin Chițu; Daniel Ciprian Vasile; Tudor Ioan Honceriu; **Paul Svasta**, Algorithm to Design Conductive Mesh for Tamperproof Envelope, 2020 IEEE 26th International Symposium for Design and Technology in Electronic Packaging (SIITME), 21-24 Oct. 2020, IEEE eXplore DOI: 10.1109/SIITME50350.2020.9292275
50. Sorin Chițu; Daniel Ciprian Vasile; Ionuț Daniel Trămândan; **Paul Svasta**, Key Expansion in Cryptographic Systems, 2020 IEEE 26th International Symposium for Design and Technology in Electronic Packaging (SIITME), 21-24 Oct. 2020, IEEE eXplore DOI: 10.1109/SIITME50350.2020.9292242
51. Loredana-Maria Burciu; Radu-Petru Fotescu; Rodica Constantinescu; **Paul Svasta**, Intelligent Warning System for Drivers, 2020 IEEE 26th International Symposium for Design and Technology in Electronic Packaging (SIITME), 21-24 Oct. 2020, IEEE eXplore DOI: <https://doi.org/10.1109/SIITME50350.2020.9292177>
52. Radu-Petru Fotescu; Loredana-Maria Burciu; **Paul Svasta**; Rodica Constantinescu, Intelligent System for Vehicle Recognition, 2020 IEEE 26th International Symposium for Design and Technology in Electronic Packaging (SIITME), 21-24 Oct. 2020, IEEE eXplore DOI: 10.1109/SIITME50350.2020.9292183
53. Mihai Branzei; Ciprian Ionescu; Bogdan Mihailescu; **Paul Svasta**; Gaudentiu Varzaru; Razvan Ungurelu; Marin Gheorghe, Investigations at the Interface of a Multilayer Structure Made of Non-Conductive and Conductive Resins, 2020 IEEE 26th International Symposium for Design and Technology in Electronic Packaging (SIITME), 21-24 Oct. 2020, IEEE eXplore DOI: 10.1109/SIITME50350.2020.9292289
54. Monica Ciolacu, **Paul Svasta**, Daniela Hartl, Stefanie Görzen, Education 4.0: Smart Blended Learning Assisted by Artificial Intelligence, Biofeedback and Sensors, 2020 International Symposium on Electronics and Telecommunications (ISETC), 5–6 November 2020, Timisoara, Romania, Page 1

Text books

1. **Svasta P.** et al., *Passive electronic components and circuits (Componente si circuite pasive)*, UPB 1998, application selection, coordination and pp. 3-21, 89-108;
2. **Svasta P.** et al., *Elements of computer aided design of electronic modules (Elemente de proiectare asistata de calculator a modulelor electronice)*, UPB 1998, coordination 5-11, 160-165;
3. **Svasta P.** et al. *Computer aided design of electronic modules (Proiectarea asistata de calculator a modulelor electronice)*, editor and coordination, "Tehnica" Publishing House 1998;
4. **Svasta P.**, I. Cristea, *Interconnection technologies through surface mount technologies (SMT) of electronic components (Tehnologiile de interconectare prin montarea pe suprafata (SMT) a componentelor electronice)*, UPB, ISBN 973-0-00888-4;
5. **Svasta P.**, Golumbeanu V., *Passive electronic components and circuits - Resistors (Componente și circuite pasive – Rezistoare)*, UPB 2000;
6. **Svasta P.**, Golumbeanu V. et al., *Up-to-date passive electronic components packaging (Noutăți în packagingul componentelor electronice pasive)*, "Politehnica Press" Publishing House - Bucharest, 2001, ISBN 973-85237-2-9;
7. **Svasta P.**, Golumbeanu V. et al., *Elements of electronic module packaging (Elemente de packaging al modulelor electronice)*, PrinTech Publishing House – Bucharest, ISBN 973-652-622-4;
8. **Svasta P.**, Golumbeanu V., Ionescu C., Fleșchiu A., Moraru D., Codreanu N. D., Leonescu D., *Passive electronic components – applications ("Componente electronice pasive - probleme")*, Cavallioti Publishing House -Bucharest, 2004, 208 pages;
9. **Svasta P.**, Golumbeanu V., Ionescu C., Vasile A., *Passive electronic components – Resistors ("Componente electronice pasive - Rezistoare")*, Cavallioti Publishing House -Bucharest, 2005, 327 pages, ISBN 973-9463-93-2;
10. **Svasta P.**, **Golumbeanu V.**, **Ionescu C.**, **Fleșchiu A.**, **Moraru D.**, **Codreanu N. D.**, **Leonescu D.**, *Passive electronic components – applications ("Componente electronice pasive - probleme")*, Cavallioti Publishing House -Bucharest, 2007, 208 pages, ISBN 973-9463-74-6;
11. **Svasta P.**, **Golumbeanu V.**, **Ionescu C.**, **Vasile A.**, *Passive electronic components – Resistors –Properties, Structure, Technology, Applications ("Componente electronice pasive – Rezistoare - Proprietăți, Construcție, Tehnologie, Aplicații")*, Cavallioti Publishing House -Bucharest, 2007, 337 pages, ISBN 978-973-7622-39-6.

List of Patents:

1. Inventor(s): SVASTA P, NICOLESCU I, DRAGOMIRESCU M, SOFRON E, STAICULESCU S, SCHIOPU P, DRAGOMIRESCU O, COLUMBEANU V, „**Automatic monitor of electric power points - has programmed individual supply and fault detector circuits, microcomputer and real time analyser**”, Patent Number(s): RO104660-A, International Patent Classification: G07C-003/02

Patent Details:

Patent Number	Publ. Date	Main IPC	Week	Page Count	Language
RO104660-A	30 Aug 1995	G07C-003/02	199617	Page: 1	

2. Vasile Cătuneanu, Vasile Corlățeanu, Marin Drăgulescu, Marieta Dragomirescu, Ioan Nicolescu, **Paul Svasta**, Ovidiu C. Dragomirescu, „**System for information transmission in sports competitions**”, patent no. 73337/11.06.1979
3. Vasile Cătuneanu, Vasile Corlățeanu, Marin Drăgulescu, Ovidiu C. Dragomirescu, Marieta Dragomirescu, **Paul Svasta**, Ioan Nicolescu, „**Display box with thyristor memory**”, patent no. 77479/29.05.1981

Expertise in national/international programs:

Program/Project	Position	Date:
Building Relations to go International for Data-Driven Growing Enterprises (start-ups and SMEs) – BRIDGE (APTE)	Junior expert - Partner	2020-2022
Managerial Structure for a Robust Technological and Innovative Environment – SMARTI (APTE)	Team Member – CO	2018-2020
DA-SPACE - Open Innovation to raise Entrepreneurship skills and Public Private Partnership in Danube Region	President - Partner	2017-2019
eDIGIREGION : Realising The Digital Agenda Through Transnational Cooperation Between Regions, FP7-REGIONS-2012-2013-1 (APTE)	Project leader	2013-2015
Integrated Development 4.0 (iDev40)	Project leader - Partner	2019-2021
NoE FlexNet - Network of Excellence for building up Knowledge for improved Systems Integration for Flexible Organic and Large Area Electronics (FOLAE) and its exploitation	Project leader	2010-2012
e-Training Microsystems Technologies – mSysTech, Lifelong Learning Program - Leonardo da Vinci	Team Member – CO	2009-2010
ELECT2EAT – E-Learning Education and Continuing Training to Electronics Assembling Technology - Lifelong Learning Program 2007-2013 Leonardo da Vinci	Project leader - Partner	2007-2013
Bilateral Project ROMANIA-HUNGARY , Investigation of “ROHS” conformal electronic micro-modules based upon the energetic efficiency method.	Project leader	2008-2009
Bilateral Project ROMANIA-SLOVENIA , Development of advanced pressure sensors and transducers	Project leader	2008-2009
Active workforce: increasing employment in the electronics and mechatronics field through training and skills development - Activ pe piața muncii: Formarea și dezvoltarea de competențe pentru creșterea ocupării în electronică și mecatronică, POSDRU/125/5.1/S/133562	Project leader	2014-2015
Advanced Collaborative Support Entity for Product and Service Innovation - Entitate Suport Colaborativ Avansat pentru Inovare de Produse și Servicii – ESCA-IPS 1/CLT/800006/8/12.05.2014 / Cod SMIS: 49741 (APTE)	Project leader	2014-2016
"Motor brushless fara senzor de pozitie cu controller electronic incorporat comandat pe Power Line" (BLCPL)	Partner	2014-2016
Family of Tactical Riflescopes with High Zoom Factor - Familie de lunete de ochire tactice cu factor zoom mare (LUTAZ)	Partner	2013-2015
Automated interactive system for optimizing water, nutrient and pesticide inputs, in order to increase the technical and economic competitiveness of small and medium vegetable farms - Sistem interactiv automatizat pentru optimizarea inputurilor de apa, nutrienti si pesticide, in vederea cresterii competitivitatii tehnico-economice a fermelor legumicole mici si mijlocii (SIOPTEF)	Partner	2012-2016
COMPLEX OPTOELECTRONIC SYSTEM FOR MULTISPECTRAL OBSERVATION AND ANALYSIS - SISTEM OPTOELECTRONIC COMPLEX PENTRU OBSERVARE ȘI ANALIZĂ MULTISPECTRALĂ” (SOIM)	Partner	2014-2016
Promoting the entrepreneur culture, adaptability, dynamism, initiative in Electronic Industry, EUROPEAN SOCIAL FUND – Operational Area Program for the Development of Human Resources 2007 – 2013	Project Manager	2009-2011
Scientific network and ecological technological platform for electronic packaging - RESPLATEPE, CEEX, AMTRANS	Project leader - Partner	2006-2008
IN-TECH TRANSFER: Integration of the resources from research institutes and universities in a modern network based on TIC, in view of support for innovation,	Team Member - CO	2006-2008

technologic transfer and accessing the Program "Competitiveness and Innovation", Lisboa strategy 2007-2013, CEEEX		
Modular system of optoelectronic sensors for acquisition, transfer and image processing in the visible and IR range - OPTELLA	Team Member	2007-2010
Bioanalytic system for the detection of pesticides residues from complex matrices of food SCREENFOOD	Team Member	2007-2010
Miniature Neuronal Radiometers/ Gamma with Solid Body destined for Radioprotection, Environment Monitoring and Fight against the traffic with radioactive substances (RMNGCS)	Team Member	2007-2010
Model and program for the determination of the innovation degree of SMEs - INNOINDEX	Team Member	2007-2010
„Multimedia System Remix Games Board"	Project Leader	2006
CEEEX – Research regarding Innovation and Optimization of hydraulic equipments and systems using mechatronic concepts, mathematical model and numerical simulation in view of performance enhancement	Partner	2005-2007
CEEEX - Research regarding the elaboration of new integrated mechatronic technologies for complex parameter measurements of the production processes, designated for hydro-pneumatic equipments	Partner	2005-2007
CEEEX – Development of a dissemination structure of the Romanian research in the area of the hydraulics, pneumatics and mechatronics for Agricultural Machinery in order to increase its visibility and to integrate it in the European Union	Project leader - Partner	2005-2007
Project of Offer for Technological Micro-Electronics in the University – PROMETEU, project INFRATECH, contr. no. 14/08.11.2004	Project Manager	2004-2005
Informational Society Program – INFOSOC C4, with the title "Integrated Technological Environment designed for the development of performing electronic modules for communications and collaborative work in the informational society ", Finance Contract no. 102/17.09.2003	Project Manager	2003-2005

Member of the professional associations:

- IEEE- senior life member (2018)
- IEEE- CPMT Hu&Ro Joint Chapter – Junior Past Chair
- IEEE-CPMT Student Branch Chapter - Advisor
- IEEE - Educational Society, member
- IEEE – EMC Romanian Chapter
- IMAPS - German Chapter, member
- IMAPS Romania Chapter, president
- IPC - member
- AFCEA – member, Chair of the Electronics Branch
- AFCEA Politehnica University Bucharest Student Chapter - Advisor
- OE-A member
- ELINCLUS - ELectronic INnovation CLUSter, Bucharest, Romania - President
- CLUSTERO, Romanian Clusters Association, Bucharest, Romania

Member of the committees:

- for organizing the International Electronic Systemintegration Technology Conference, Chair of the Subcommittee for Passive Components;
- European Liason Committee of IMAPS Europe
- for organizing the International Spring Seminar on Electronic Technology Dresden, Budapest, Wroslaw, Sofia, Viena, Bucharest, Prague, Bratislava ISSE-member of the Steering Committee ;
- of the publication committee for the “Acta Tehnica Napocensis Electronics and Telecommunications” publication;
- for organizing the International Symposium for Microelectronics Technology and Microsystems MTM, Poland, Ukraine, Slovakia, Germany, Romania member of the Scientific Committee (1998-2002);
- for organizing the international conference SIITME (www.siitme.ro) –General Chair and Founder
- Scientific Committee of the Research Institute for Hydraulics and Pneumatics.

Member of:

- Founding member and Chair of the Association for Promoting the Electronic Technology, APTE
- Founding member and vice-chair of the SOFDAAD Society 1994
- Member of the Romanian Society for Concurrent Engineering
- Member of the Romanian Association for Electromagnetic Compatibility, ACER, 2001
- Member of the National Association for Cryptography, 1998
- Member of the Scientific Council of the National Center for Information Technology
- Vice-Chair of the Romanian Association for the Electronic Industry and Software, ARIES

Awards:

- AGIR Award on 1998 for the work "Computer-aided design of electronic modules", publisher and coordinator, 1998 Technical Publishing
- The National Order "Faithful Service" in the rank of officer. 2000
- Diploma of Center of Excellence granted by the Ministry of Education and Research, 2001
- IEEE-CPMT Society, 2003 Best Student Branch Chapter of the Year Award, Paul Svasta, Advisor,
- Certificate of Achievement granted in January 2004 by IEEE Sectiunea Romania
- Diploma of Excellence granted by ARIES for the promotion of students and their involvement in research and development activities, 2004
- Authorization in March 2004, within UPB-CETTI, of the Technology and Business Incubator: CETTI-ITA as an entity in the structure of ReNITT (National Network of Innovation and Technological Transfer)
- **24 february 2012 – awarding the title of Doctor Honoris Causa of the Tehnical University of Cluj Napoca**
- **29 april 2014 – awarding the title of Doctor Honoris Causa of the University of Pitești**

- **June 2021- 2021 IEEE EPS David Feldman Outstanding Contribution Award, for strongly promoting and implementing Electronics Packaging and IEEE EPS activities in Europe, Region 8, for establishing a functional networking bridge from Academia to Industry**

Accreditations and licenses:

- Certified as “*Expert Evaluator*” by the National Council for Higher Education Scientific Research (CNCSIS), (May 1999)
- PhD supervisor
- Professor Emeritus at University Politehnica of Bucharest
- Member of the scientific board at:
 - International Spring Seminar for Electronic - ISSE (1997- to date),
 - International Symposium for Design and Technology of Electronic Packages - SIITME (1995- to date).
 - Microreliability and Nanoreliability in Key Technology Applications Congress (2006-to date),
 - Electronics Systemintegration Technology Conference (2006- to date)