

## Personal information

First name(s) / Surname(s) **Dinu Coltuc**

E-mail(s) [coltuc@valahia.ro](mailto:coltuc@valahia.ro)

Date of birth **[REDACTED]**



## Work experience

Dates 01/10/2000→

Occupation or position held Professor with the Electrical Engineering, Electronics and Information Technology Faculty, Director of the Doctoral School in Engineering of UVT (since nov. 2011), Director of the Research Centre for Electrical Engineering, Electronics and Information Technology (since 2009), Head of Electronics and Telecommunications Dept. (2002- nov. 2011)

Main activities and responsibilities Teaching: Image Processing, Signal Processing. Research in image processing, watermarking  
Name of employer Valahia University Targoviste (Romania)

Dates 01/09/2009 – 30/11/2009, 01/10/2007 - 31/12/2007

Occupation or position held Invited Professor

Main activities and responsibilities Teaching: Image Segmentation and Analysis, Research in reversible watermarking  
Name of employer Ecole Nationale Supérieure d'Ingénieurs pour l'Energie, l'Eau et l'Environnement & GIPSA-Lab (2009), Ecole Nationale Supérieure d'Ingénieurs Electriciens de Grenoble & LIS (2007), Grenoble INP

Dates 01/10/2005 - 31/08/2006

Occupation or position held Associate Researcher CNRS

Main activities and responsibilities Research in Reversible Watermarking, Teaching Image and Signal Processing  
Name of employer Laboratoire des Images et des Signaux – LIS, Grenoble (France)

Dates 01/02/2000 - 31/07/2000, 01/02/2001 - 31/07/2001, 01/02/2002 - 31/07/2002

Occupation or position held Invited Professor PAST (18 months)

Main activities and responsibilities Teaching: Image Processing, Signal Processing. Researches in Robust Watermarking  
Name of employer Ecole Nationale Supérieure d'Ingénieurs Electriciens de Grenoble & LIS, INP Grenoble

Dates 01/09/1997 - 31/12/1997, 01/09/1998 - 31/12/1998, 01/09/1999 - 31/12/1999

Occupation or position held Invited Professor (12 months)

Main activities and responsibilities Teaching: Signal Processing and Image Processing. Research in fast algorithms, watermarking  
Name of employer Ecole Supérieure d'Ingénieurs d'Annecy, Université de Savoie (France)

Dates 01/11/1993 - 31/01/1995

Occupation or position held Associate Researcher

Main activities and responsibilities Research in nonlinear filters, mathematical morphology, algorithms for signal/image processing  
Name of employer School of Electrical and Computer Engineering, Aristotle University of Thessaloniki (Greece)

Dates 01/09/1984 - 30/09/2000

Occupation or position held Researcher (1984-1998), Senior Researcher (since 1998), Deputy (Feb. 1991 to March 1992, Feb. 1995 to Feb.1996) and Head (Apr. 1992 to Nov. 1993) of the Sensors and Transducers Dept.

Main activities and responsibilities Principal investigator in more than 10 research projects on Image Processing, Image Analysis, Pattern Recognition, Supervised Learning, Image Coding, Fast Algorithms, Image Algebra, etc.

Name and address of employer Research Institute for Electrical Engineering, Bucharest, Romania

## Education and training

Dates 1977 - 1982

Title of qualification awarded Electronics Engineer (MSc)

Name of organisation Faculty of Electronics and Telecommunications, University "Politehnica" of Bucharest.

Dates 1995 - 1997

Title of qualification awarded PhD in Electronics and Telecommunications

Name of organisation Faculty of Electronics and Telecommunications, University "Politehnica" of Bucharest.

## Personal skills and competences

Mother tongue(s) **Romanian**

Other language(s)

Self-assessment  
*European level (\*)*

**English**

**French**

Understanding				Speaking				Writing	
Listening		Reading		Spoken interaction		Spoken production			
C2	Proficient user	C2	Proficient user	C2	Proficient user	C1	Proficient user	C2	Proficient user
C2	Proficient user	C2	Proficient user	C2	Proficient user	C2	Proficient user	C1	Proficient user

(\*) [Common European Framework of Reference \(CEF\) level](#)

## Additional information

-Associate editor for *IEEE Transactions on Information, Forensics and Security* (since Nov. 2016) and *Journal of Visual Communication and Image Representation* (since Oct. 2016)

-Member of the *National Council for Attestation of University Titles, Diplomas and Certificates* (CNATDCU), Electronics and Telecommunications Engineering (since 2016).

-Co-recipient of the Romanian Academy prize "Traian Vuia" for Contributions to the development of digital image processing systems (1987)

-Outstanding Editor Award, *Journal of Visual Communication and Image Representation*, 2019,

-Best Associate Editor Award, *Journal of Visual Communication and Image Representation*, 2021.

-PI in 14 national research projects. Participant in 6 national projects and 3 international projects

-Senior IEEE Member (since 2011).

-Served as reviewer for IEEE Trans. on Image Processing, IEEE Trans. on Information Forensics and Security, IEEE TCSVT, IEEE Trans on Multimedia, IEEE Trans. on PAMI, IEEE Signal Processing Letters, Journal on Systems and Software, Signal Processing, JVCi, DSP, IET Image Process., etc.

01/02/2022