Curriculum Vitae Europass

Personal Information

Last Name, **First Name**	ŢĂPUŞ Nicolae
Address	██████████████████████
██	██████████████████ Mobil:
Fax	+40214029333
E-mail	Nicolae.Tapus@cs.pub.ro
Nationality	ROMANIAN
Birth Date	
Sexe	M

Current Position	Full Professor, Politehnica University Politehnica of Bucharest (Romania); Computer Science and Engineering Department
Research Area	

Professional Experience

Oct. 1992 – present	Full Professor
	Research and Teaching
	Politehnica University of Bucharest (Romania); Computer Science and Engineering Department
	Splaiul Independentei , 313, 060042 Bucharest, Romania
	Tel:+40 (0)21 402 9100, +40 (0)21 318 10 22 / 23 / 24 / 25
Oct. 1990 – 1992	Associate Professor
	Research and Teaching
	Politehnica University of Bucharest (Romania); Computer Science and Engineering Department
	Splaiul Independentei , 313, 060042 Bucharest, Romania
	Tel:+40 (0)21 402 9100, +40 (0)21 318 10 22 / 23 / 24 / 25
Oct. 1978 – 1990	Assistant Professor
	Research and Teaching
	Politehnica University of Bucharest (Romania); Computer Science and Engineering Department
	Splaiul Independentei , 313, 060042 Bucharest, Romania
	Tel:+40 (0)21 402 9100, +40 (0)21 318 10 22 / 23 / 24 / 25
Oct. 1972 – 1978	Teaching Assistant
	Research and Teaching
	Politehnica University of Bucharest (Romania); Computer Science and Engineering Department
	Splaiul Independentei , 313, 060042 Bucharest, Romania
	Tel:+40 (0)21 402 9100, +40 (0)21 318 10 22 / 23 / 24 / 25

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	Education
Oct. 1976 – July 1982	PhD in Computer Scinece
	Research topics: Microprogramming; Algorithms; Computer programming; Architectures and computer systems; Computer; Distributed Systems, Hardware Design; Mathematics; Physics
	Politehnica University of Bucharest (Romania); Computer Science and Engineering Department
	National / International Level: ISCED 6
Oct. 1967– Sept. 1972	Diplomat Engineer Diploma
	Computer Architecture, Operating Systems, Databases, Communication Systems, Computer Programming, Microprocessors. Mathematical Foundations of theoretical computer science. Design, lifecycle management, systems integration and integrity of contextual hardware, software and communications. Analysis and solve engineering problems, using the tools of computer science.
	Politehnica University of Bucharest (Romania); Computer Science and Engineering Department
	National / International Level: ISCED 6
Sept. 1963 – June 1967	Baccalaureate National College "Nicolae Balcescu" Craiova High school
June 1983 – Sept. 1983	Fulbright Program
	Microprocessor-based computing systems
	University -Provo, Utah; US, University of Michigan, Ann-Arbor Michigan, US
	National / International Level: ISCED 6
Mar 1995– Sept. 1995; April 1996– Sept. 1996	TEMPUS Program
	Parallel and Distributed Systems and Architectures Parallel Computing Architectures INPG Grenoble France; Free University of Amsterdam, Netherlands Polytechnic Institute of Torino, Italy National / International Level: ISCED 6

PERSONAL SKILLS

Mother Tongue | Romanian

Other Languages | English, French

Autoevaluare

European Level(*)

	Understanding				Speaking				Writing	
	Listening		Reading		Spoken Interaction		Spoken Production			
English	C1	Proficient User	C1	Proficient User	C1	Proficient User	C1	Proficient User	C1	Proficient User
French	B1	Independent User	B1	Independent User	B1	Independent User	B1	Independent User	B1	Independent User

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Teaching	He developed an extensive teaching activity in the Romania and abroad. His lectures were always appreciated by students at the Politehnica University of Bucharest • Digital Computer and programming, • Computer Architecture, • Structure numerical data processing systems • System Design Based on Microprocessors • Structure of Computer Systems • Computer Systems Architecture • Local Area Networks • Parallel Computing Architecture • Parallel Processing Architectures • Parallel and Distributed Systems • Cluster and Grid Systems Architecture and at the University of Maryland in USA • CMIS 310 Computer Systems and Architecture • CMIS 141 Introductory Programming • CMIS 102 Introduction to Problem Solving • CMIS 242 Intermediate Programming • CMIS 435 Computer Networking
Commiunication Skills	Interaction with people of different nationalities in the teaching and research activities. Collaboration with people from various cultures and educational and cultural environments present in the teams I was part of (Romania, Netherlands, France, Italy, USA). Professional and social interaction with communities at the Politehnica University of Bucharest, in the country and abroad.
Organisational / managerial skills	Ability to coordinate activities and collectives of people, manifested in projects, and in scientific and administrative functions. Among the recent activities, involving skills and organizational skills, can be mentioned the following: • Vice President of Information Technology and Communications - Computer and Telecommunications **Academy of Technical Sciences** of Romania from 2009 to present; • President IEEE Romania Section (2005-2009); • President of Computer Societu Chapter IEEE Romania Section; • Coordinator Computers Chapter - IEEE - Region 8 (Africa, Europe, Middle East) • Member of the Senate of the Politehnica University of Bucharest; • Coordinator of the International Centre for Training in Cisco, CATC CISCO Academy, which prepares instructors to 43 regional academies of 12 countries in Eastern Europe and East Asia and coordinates the work of 271 Local Academies; • Coordinator Training Center in distributed computing - CIID the Politehnica University of Bucharest; • Director of the International Programming Contest ACM South East Europe region from 1994 to date; • Vice-Rector of the Politehnica University of Bucharest 2006 – 2012; • Vicepresident of the Senate of the Politehnica University of Bucharest (2012-present); • Head of Computer Science and Engineering Department, Faculty of Automatic Control and Computers (1990-2008,2012- present). • Supervisor of PhD in Computer and Information Technology. • Member of the CNATDCU (National Council for Titles, Diplomas and Certificates)Commission in Computer and Information Technology.
Membership in professional	• Member of the **Academy of Technical Sciences of Romania**;

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societies	• Senior Member in IEEE The Institute of Electrical and Electronics Engineering; • Senior Member in the Association of Computing Machinary ACM; • Information Society Foundation - Founding Member • Romanian Society of Informatics; • Romanian Society of Automation and Information Technology;
Member of the editorial board of the journals	• Proceedings of the Romanian Academy, http://www.acad.ro/sectii2002/proceedings/proc_pag_eb.html • Romanian Journal of Information Science and Technology – Romanian Academy ISSN: 1453-8245, http://www.ear.ro/3brevist/rv82/rv82.htm , http://www.imt.ro/romjist/ • Transactions on Automatic Control and Computer Science ISSN 1224-600X , http://www.ac.upt.ro/journal/ • JAQM - Journal of Applied Quantitative Methods, ISSN 1842-4562, • http://jaqm.ro/index.php • Romanian Journal in Computer Science and Automation, http://www.ici.ro/rria.html • Automation, Computers and Applied Mathematics (ACAM) Cluj Napoca • Scientific Bulletin PUB - Electrical Engineering and Computer Science http://www.pub.ro/romana/Buletin_stiintific/index.html

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Job-related skills	He designed computer systems and peripherals and has a patented inventions.
	Based on research grants, he developed firmware microcomputer MC1, the first of its kind in the Romania, which was used to ICEMENERG to reduce electricity consumption (1973)
	As a result of scientific research conducted in the Department of Computer Science and Engineering, in collaboration with specialists from industry, the following microcomputers based on microprocessors have been introduced in series production at the Computers Factory in Bucharest:

- microcomputer FELIX MC8 (1974) - based on the INTEL 8008 microprocessor;
- microcomputer FELIX M18 (1976) - based on the INTEL 8080 microprocessor;
- microcomputer FELIX M18B (1978) - based on the INTEL 8080 microprocessor;
- microcomputer FELIX M118 (1980) - based on the INTEL 8080 microprocessor;
- microcomputer FELIX M216 (1982) - based on the dual processor INTEL 8086 and 8080 ;
- microcomputer FELIX FELIX-PC (1984) – first desktop in Romania based on the INTEL 8086 microprocessor;

These microcomputers based on microprocessor were manufactured in the same time with the other branded companies at the world wide level. For Romania was an innovation and a premiere.

FELIX microcomputer M118 attended the International Fair in Leipzig, where it was gold medalist and International Fair SICOP in France, after which it was requested and built for exports in China and several other Eastern European countries.

Prof. Tapus had an important role in research, pioneering activity in Romania, for the design and implementation of computer systems based on microprocessors, which contributed to the development of applications in all areas, the spread of computers in many research, design, education, and work towards a new paradigm of interactive computing.

Prof. Tapus was part of the team who made the invention entitled "Device for entering graphic information in an electronic computer" (aka mouse), which has been recognized by granting patent no. 92432 of 27.4.1987

He conducted the development in the National Center for Information Technology, the most academic performance heterogeneous cluster in our country, which contains about 3 200 cores and over 36 TB storage and communication 10Gps. Provides infrastructure for research activities at national and international level, as a point of attracting young researchers and members of the Department of Computer.

National Awards

Following the recognition of the work done and the results obtained in the design and implementation of new computer systems type microcomputer he was appreciated by receiving:

- Award "Traian Vuia" awarded by the Romanian Academy in 1977,
- Ministry of Education Award, granted in 1988

International Awards

He participated in the development of complex projects with students and Ph.D. students on advanced computing systems, embedded systems, distributed systems, networking and his work was appreciated by the scientific community by providing international awards:

- CENIC, the Corporation for Education Network Initiatives in California, CENIC 2006 Innovation Award for High-Performance Applications)
- Faculty Award recipients-IBM, 2008 and medals at several international contests:
- First place in the IEEE Annual Computer Society International Design Competition (CSIDC), BE Secure - Building Surveillance Equipment, with a student team, 25 – 28 June, 2002, Washington, DC, USA;
- 2nd place in the IEEE Annual Computer Society International Design Competition (CSIDC) Eyes Only, with a student team, 25–28 June, 2003, and eXpress! Help, (CSIDC) 25–28 June, 2004, Washington, DC, USA;

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	- 3rd place in the IEEE Annual Computer Society International Design Competition (CSIDC), NOMAD Positioning System, 25–27 June, 2005, and (CSIDC), Synairgy, with a student team, June 29, 2006 Washington, DC, USA; - Imagine Cup Software Design, 5th place in he Regional Competition for Central and Eastern Europe, April 2004, WebEdu - A Collaborative Online Learning Environment, with a student team, Cambridge, England; - 3rd place Windows Embeded, Seattle USA, Invisanet, with a student team, 20 June 2005; - 3rd place Windows Embeded, Seattle USA, BirdSpot, with a student team, 22 June 2006
Grant Proposals	He participated as a researcher, project manager, and project coordinator for several European projects, among which: - FP7 HPSEE- High-Performance Computing Infrastructure for South East Europe's Research Communities 2012-2014 - FP7 / P2P Next - Next Generation Peer-to-Peer Content Delivery Platform; Project Coordinator; 2008-2011; - FP7 / SENSEI- Integrating the Physical with the Digital World of the Network of the Future; 2008-2011; - NATO / Development of ICT through establishing an educational Alliance of Technical Universities of Ukraine under KPI, Networking Infrastructure Grant; Project Manager; 2008-2009; - FP6 / EGEE: Enabling Grids for E-sciencE; Director proiect; 2005-2009; - FP6 / SEE-GRID South-Eastern European Grid-enabled eInfrastructure Development; Project Manager; 2005-2009; - FP6 /" EU-NCIT : NCIT leading to EU IST excellency; Project Manager; 2005- 2008; - TEMPUS SJEP-07101; Project Manager; 1994-1997 - TWISNet - Trustworthy Wireless Industrial Sensor NETworks - ERRIC—Empowering Romanian Research on Intelligent Information Technologies, FP7-REGPOT-2010-1, No. 264207, 2010-2013 - INRIA "Associate Teams" Programme, DataCloud@work - EGI-InSPRE Enabling Grids for the E-science in Europe

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Grant Proposals	He participated as lead researcher and project manager in a series of projects from the national research programs among which:

- PARADIS integrated environment for developing parallel and distributed systems
- Integration of "wireless" stations in computer networks
- Educational services in the Internet and Intranet
- Research and development solutions for distributed systems performance
- Development Analysis Grid architectures
- Technical specifications for metropolitan network backbone
- Developing a core based on Grid technologies and applications for high performance computing
- AeroLink - High-performance network for research in aeronautics
- IComGrid - Infrastructure for Grid pilot communications
- Monitoring distributed middleware and applications of National Grid infrastructure (MON-I)
- Monitoring Distributed National Grid infrastructure resources (MON-R)
- Integrated system for dynamic resource discovery and management of grid infrastructure
- PALIROM - concept, design and implementation of an application package for analysis and processing language texts written in Romanian
- SINDEGRID - national system development and operation of grid computing LCG for particle physics
- SISEB - System security of electronic banking (e-banking) through biometric signature
- System integration of parallel applications in GRID environment

Computer Skills	Microsoft Office ; Unix: Limbaje de asamblare, Matlab, Python, Latex, si SQL Java, C/C++,. Windows: C++ Builder, Jbuilder, Microsoft Studio, Java and Parallell programming: MPI, OpenMP, Pthreads si Java Threads.

Driving Licence	B – car driving Licence

Publications	His competences in research, have been materialized through a series of papers published in the proceedings of prestigious national and international conferences. He published:

- 28 books and teaching textbooks;
- 250 publications published in well-known journals and peer-reviewed conferences

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Annexes	Liste of Publications Diplomas Awards Patents

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