

## PERSONAL INFORMATION

## Aurel-Stefan GONTEAN



✉ aurel.gontean@upt.ro

## POSITION

## WORK EXPERIENCE

- 2020 - **Head of Department, Applied Electronics**  
Electronics and Telecommunications Faculty, [www.etc.upt.ro](http://www.etc.upt.ro), 300223 Timisoara, ROMANIA
- 2009 - **Visiting Professor**  
Baden-Wuerttemberg Cooperative State University Loerrach, [www.ba-loerrach.de](http://www.ba-loerrach.de), Germany  
**Courses:** Computer Architectures; Microcontrollers; Advanced Electronics
- 2004 - 2012 **Vicedean**  
Electronics and Telecommunications Faculty, [www.etc.upt.ro](http://www.etc.upt.ro), 300223 Timisoara, ROMANIA  
**Responsibilities:** IT Infrastructure, Funding monitoring, cooperation with economic partners
- 2008 - 2009 **EU expert for evaluating project proposals**  
Scientific Research Fund, Sofia, Bulgaria,
- 2008 - **PhD Advisor**  
6 PhD Thesis delivered; 6 under development  
**Sector** Electronics Telecommunications and Information Technologies
- 2005 - **Professor**  
Politehnica University of Timisoara, [www.upt.ro](http://www.upt.ro), Electronics and Telecommunications Faculty, [www.etc.upt.ro](http://www.etc.upt.ro), Applied Electronics Department, 2 V. Parvan Bv., 300199 Timisoara, ROMANIA  
**Courses** Digital Integrated Circuits, Microcontrollers, Programmable Logic Systems, VB.net
- 2004 - **National expert for evaluating project proposals**  
National Agency for Scientific Research, ANCS, [www.ancs.ro](http://www.ancs.ro)
- 1994 - 2000 **Manager**  
Saratoga Ltd., Timisoara, ROMANIA
- A 35 employees IT company
  - **Main sector:** computer networks

## EDUCATION AND TRAINING

- 2014 **EuroTraining (Timisoara, Romania)**
- Microelectronics
- 2009 **Agilent Trainings (Holland)**

- RF Advanced Measurements
- 2008 **Labview Trainings**
  - Basics, DAQ, FPGA
- 1998 **PhD Degree**  
Politehnica University of Timisoara, ROMANIA
  - FPGA Design
- 1996 **3 months scholarship** at Central Lancashire University, Preston, England
  - VLSI Design oriented
- 1995 **3 months scholarship** at Fachhochschule Wiesbaden, Germany
  - Programmable Logic Design oriented
- 1993 **3 days FPGA Workshop**, Texas Instruments, Germany
  - FPGA Design
- 1986 **Engineer**  
Politehnica University of Timisoara, Electronics and Telecommunications Faculty, Timisoara, ROMANIA

PERSONAL SKILLS

Mother tongue(s) Romanian

Other language(s)

	UNDERSTANDING		SPEAKING		WRITING
	Listening	Reading	Spoken interaction	Spoken production	
English	C2	C2	C2	C1	C1
German	B1	B2	B1	B1	B1
French	B1	B2	A2	A2	B1

Levels: A1/2: Basic user - B1/2: Independent user - C1/2 Proficient user  
Common European Framework of Reference for Languages

Managerial skills Manager both in private and public domains, over 12 years of experience

Computer skills
 

- Matlab, LabVIEW, MS Access, Visual Basic.net, C
- CorelDraw

Design
 

- CAD (Eagle, LT Spice, VHDL)
- Embedded Systems (Hardware, Programming)
- Digital design, FPGA applications and PLC applications

Driving licence B Category

## ADDITIONAL INFORMATION

Publications	150+ papers (101 ISI WoS, 160+ Scopus), 8 books, 3 international books (chapters), 25+ contracts and grants, 260+ citations, 4 patents, 2 patents pending
Projects: 2008 -	Head of VLSI Lab in the Renewable Energies Research Institute, Timisoara, Romania. <ul style="list-style-type: none"><li>The VLSI Lab is 1.5 million Euro funded and the whole Institute is an EU 15 million Euro investment</li></ul> 2017-2018 Thermo-Electric Hybrid Solar System, PN-III-P2-2.1-PED-2016-0074, Value: 499700 Lei (110800 EUR), <a href="http://fess.upt.ro">http://fess.upt.ro</a>
Reviewer	Energy Conversion and Management - ISI Journal (2013 - present) TSP, PDS, ISETC - ISI conferences (2004 - present) ISSE, SIITME, ISSE - IEEE Conferences (2008 - present)
Honours and awards	Best paper Award IDAACS 2011 IEEE Award, 2014 IEEE Senior Member, 2019 Gold Medal, Salon International des Inventions, Genève, 2019 Gold Medal, Invention Contest, Timisoara, Romania, 2019
Memberships	<ul style="list-style-type: none"><li>AFCEA (2013 - )</li><li>EPETRUM (2009 - )</li><li>IEEE (1999 - )</li><li><math>\pi</math> - TEAM, Texas Instruments, Europe (1994 - )</li></ul>
References	<ul style="list-style-type: none"><li>Prof. Dr.Ing. Axel Sikora, University of Applied Sciences, Offenburg, Germany, <a href="http://www.hs-offenburg.de">www.hs-offenburg.de</a>, <a href="mailto:axel.sikora@hs-offenburg.de">axel.sikora@hs-offenburg.de</a></li><li>Prof. Dr.Otto Schmidt, DualeHochschule Baden-Württemberg, Loerrach, Germany, <a href="http://www.dhbw-loerrach.de/index.php?id=schmidt">www.dhbw-loerrach.de/index.php?id=schmidt</a>, <a href="mailto:schmidt@dhbw-loerrach.de">schmidt@dhbw-loerrach.de</a></li><li>Prof. Dr.Ing. Peter Fromm, University of Applied Sciences, Darmstadt, Germany, <a href="http://www.eit-h-da.de">www.eit-h-da.de</a>, <a href="mailto:fromm@eit-h-da.de">fromm@eit-h-da.de</a></li></ul>
Personal Web Page	<a href="https://sites.google.com/site/aurelgontean/home">https://sites.google.com/site/aurelgontean/home</a>
ORCID ID	<a href="https://orcid.org/0000-0002-1485-6710">https://orcid.org/0000-0002-1485-6710</a>
ResearcherID	<a href="https://orcid.org/0000-0002-1485-6710">W-3144-2017</a>
Scopus Author ID	<a href="https://orcid.org/0000-0002-1485-6710">8951598100</a>