



🔀 dan.nicula@unitbv.ro

PURPOSE	PhD thesis Committee referee				
EXPERIENCE					

1990-Present Professor

ACADEMIC

TRANSILVANIA University of Braşov

Department of Electronics & Computers

- Courses:
 - Digital Electronics
 - Hardware Description Languages
 - VLSI/ASIC Design
 - Online Communication (Sociology)

Business Education, Electronics/Computers/Telecom, Digital Media

1992 - 1997 PhD Computers TRANSILVANIA University of Brasov

1985 - 1990 Engineer, Electronics and Telecommunication Polytechnic Institute of Bucharest, Applied Electronics

INDUSTRIAL EXPERIENCE

Expert VLSI Engineer 2015-present

XPERI - FotoNation

Business Semiconductors, Digital IP

- Projects Verilog RTL Design Various image processing related IPs
 - Memory Controllers •
 - AXI arbiters and convertors •

RTL Designer, General Manager Romania 1999-2015

eASIC Corporation

Business Semiconductors

- Projects Verilog RTL Design
 - Various communication related IPs, Memory Controllers •
 - Design of control part for eASIC structured ASIC (eMU embedded Microrpocessor Unit) •
 - eScope Logic analyzer implemented with eASIC •

"How to implement a digital oscilloscope in Structured ASIC fabric"

EDUCATION						
1992-1997	PhD, Computers TRANSILVANIA University of Braşov					
	Thesis: "High Performance Architecture for Microprocessors"					
1985-1990	Engineer, Electronics & Telecommunication					
	Politechnical Institute Bucharest, www.upb.ro					
	Faculty of Electronics and Telecommunication					
	 Analog/Digital design, Video processing 					
PERSONAL SKILLS						
Native language	Romanian					
Other language(s)	UNDERSTANDING		SPEAKING		WRITING	
	Listening	Reading	Spoken interaction	Spoken production		
English	C2	C2	C2	C2	C2	
Communication skills Organisational / managerial skills	 Good communication skills gained through the experience as a General Manager Romania, Recruiter and Trainer for personnel (eASIC Corporation). Good presenter skills gained through the experience as a professor (TRANSILVANIA University of Braşov). Top-management position (10 years, General Manager) managing all activities in a small company: 					
	 employees (head hunting, selection, training), logistic (travel, team events), administration (procedures, work flows), accounting (supervision, audit), leadership (100+ employees, 10+ direct supervisor), remote reporting oversee (eASIC Corporation) <u>Supervise students for graduate and master thesises</u> since 1995 (TRANSILVANIA University of Braşov). 					
Job-related skills	 Business developer (helped and support for 4 companies to start business or open subsidiaries at Brasov in VLSI Digital IC business). Negotiation with suppliers, commercial contracts, lease (eASIC Corporation) PR & HR (eASIC Corporation) 					
Computer skills	 EDA Electronic Design Automation tools for IC implementation (front-end, HDL Simulators, FPGA implementation) 					
Other skills	 HW architecture definition for large systems (specification, coding style, RTL model, test environment) acquired during many team projects. Good understanding of local market and individual behaviour of employees. 					

- Publications <u>Dan NICULA</u>, <u>Verilog Carte de învățătură</u>, <u>Editura Universității Transilvania din Brașov</u>, 2019, ISBN 978-606-19-0563-8, (Romanian)
 - <u>Dan NICULA</u>, <u>ELECTRONICĂ DIGITALĂ Carte de învățătură 2.0</u>, <u>Editura Universității Transilvania</u> <u>din Brașov</u>, 2015, ISBN 978-606-19-0563-8, (Romanian)
 - G. TOACSE, <u>Dan NICULA</u>, <u>ELECTRONICĂ DIGITALĂ</u>. <u>Dispozitive</u>, <u>Circuite</u>, <u>Proiectare (I)</u>, <u>Verilog</u> <u>HDL (II)</u>, Editura Tehnică, București, 2005, ISBN 973-31-2270-X; 973-31-2268-8; 973-31-2269-6, (Romanian)
 - Bogdan SBARCEA, <u>Dan NICULA</u> <u>Automatic Conversion of MatLab/Simulink Models to HDL</u> <u>Models</u>, Proc. of IEEE Optim'2004, Brasov, ROMANIA
 - M. CIRSTEA, A. DINU, M. McCORMICK, <u>Dan NICULA</u>, <u>A VHDL Success Story: Electric Drive</u> <u>System Using Neural Controller</u>, VHDL International Users Forum Fall Workshop (VIUF'00) October 18 - 20, 2000, Orlando, Florida.
 - M. CIRSTEA, A. DINU, <u>Dan NICULA</u>, <u>A Practical Guide to VHDL Design</u>, Editura Tehnică, Bucharest, 2001, ISBN 973-31-1539-8.
 - Mircea Moldovan, <u>DAN NICULA</u>, Traian Tulbure, <u>Low-cost Implementation of Digital Oscilloscope in</u> <u>Nextreme™ Structured ASIC</u>, eASIC Corporation Whitepaper WP-1 Version 1.0
 - Patent <u>DAN NICULA Image processing apparatus</u>, EUROPEAN PATENT APPLICATION EP 3 582 176 A1, Date of publication: 18.12.2019 Bulletin 2019/51, Application number: 19196620.9, Date of filing: 11.09.2019

Dan NICULA