

Curriculum Vitae

Sorin Cotofana

Current Position: Associate professor with the Faculty of Electrical Engineering, Mathematics, and Computer Science, Delft University of Technology, Delft, The Netherlands.

Synopsis: 40 years of work experience in Computer Engineering varying from Computer Aided Design to Computer Architecture & Arithmetic, Logic Design, and Implementation. (Co-)authored more than 300 papers in peer-reviewed international journal and conferences, (co-)advised more than 50 master students and 20 PhD students, and received 12 international conferences best paper awards. *Current research* is focused on: (i) *unconventional computation paradigms and computation with emerging nano-devices*, (ii) design and implementation of *energy effective dependable/reliable systems out of unpredictable/unreliable components*, and (iii) *ageing assessment/prediction and lifetime reliability aware resource management*.

Education:

PhD EE 1998 **Delft University of Technology** (TU Delft), EE Department, Delft, the Netherlands
M.S. CS 1984 **"Politehnica" University of Bucharest**, Bucharest, Romania

Professional Experience:

Oct. 1984 to Sept. 1987, Research & Development Institute for Semiconductors, Bucharest, Romania, **Engineer**; *Oct. 1987 to Dec. 1989*, Research & Development Institute for Semiconductors, Bucharest, Romania, **Scientific Researcher**; *Jan. 1990 to Oct. 1993*, Research & Development Institute for Semiconductors, Bucharest, Romania, **Senior Researcher, Head of the Computer Aided Design Department**; *Oct. 1993 to March 1998*, Delft University of Technology, **Research Assistant**; *March 1998 to Aug. 2001*, Delft University of Technology, **Assistant Professor**; *Sept. 2001 to Present*, Delft University of Technology, **Associate Professor**.

Best Paper Awards: 12th IEEE/ACM International Symposium on Nanoscale Architectures, NANOARCH 2016; 12th IEEE Conference on Nanotechnology, IEEE NANO 2012; 8th ACM/IEEE International Symposium on Nanoscale Architectures, NANOARCH 2012; 15th IEEE International Conference on System Theory, Control and Computing, ICSTCC 2011; 29th IEEE International Semiconductor Conference, CAS 2006; 5th IEEE Conference on Nanotechnology, IEEE NANO 2005; Annual Workshop on Circuits, Systems, and Signal Processing, ProRISC 2002; 25th IEEE International Semiconductor Conference, CAS 2002; International Conference on Computer Design, ICCD 2001; 24th IEEE International Semiconductor Conference, CAS 2001; 21st IEEE International Semiconductor Conference, CAS 1998; 9th IEEE Annual Semiconductor Conference, CAS 1986;

Professional Activities:

Membership: IEEE *Fellow*, member of the IEEE Computer Society and IEEE CAS Society; IEEE CAS Society Distinguished Lecturer (2019-2021); IEEE CAS Society Board of Governors member (2020-2025); Member of the IEEE Circuits and Systems *Nano-Giga* technical committee, *Chair Elect 2011-2013*, *Chair 2013-2015*, *Past Chair 2015-2017*;

IEEE Editorial Appointments: IEEE Transactions on Nanotechnology (Editor in Chief 2019 -); IEEE Transactions on Computers (Associate Editor 2019 - 2022); IEEE Transactions on Nanotechnology (Associate Editor in Chief 2015 - 2019); IEEE Transactions on Nanotechnology (Senior Editor 2014 - 2019); IEEE Transactions on Multi-Scale Computing Systems (Steering Committee 2014 - 2018); IEEE Journal on Emerging and Selected Topics in Circuits and Systems (Senior Editorial Board 2016 - 2017); IEEE Transactions on Nanotechnology (Associate Editor 2008 - 2014); IEEE Transactions on Circuits and Systems I (Associate Editor 2009 - 2011);

Publications: https://scholar.google.com/citations?view_op=list_works&hl=en&hl=en&user=DkZS_dAAAAAJ&pagesize=80